

Title (en)

SWITCHED CAPACITOR SYSTEM, METHOD, AND USE

Title (de)

SWITCHED-CAPACITOR-SYSTEM, -VERFAHREN UND VERWENDUNG

Title (fr)

SYSTEME DE CONDENSATEUR COMMUTE, PROCEDE CORRESPONDANT ET UTILISATION DE CE DERNIER

Publication

**EP 1540565 A2 20050615 (EN)**

Application

**EP 03791719 A 20030820**

Priority

- US 0326198 W 20030820
- US 23211302 A 20020829
- US 23154102 A 20020829

Abstract (en)

[origin: WO2004021251A2] An apparatus and method for adding input voltage signals. First 206 and second 208 input voltage signals are respectively sampled onto first 218 and second 228 capacitors during a first clock phase 202. In response to a second clock phase 204, the first sampled input voltage 206 that is held on the first capacitor 218 is coupled to the negative input terminal 236 of an amplifier 230, and the second sampled voltage 208 held on the second capacitor 228 is coupled to the positive terminal 240 of the amplifier 230. A feedback voltage is provided from the amplifier output 216 to the negative amplifier input 236 via the first capacitor 218 during the second clock phase 204. The first 206 and second 208 input voltage signals are added at the amplifier 230 during the second clock phase 204 to output 216 the sum in response to the sampled input voltage signals and the output feedback, whereby the resulting transfer function is independent of capacitor mismatch and non-linearity.

IPC 1-7

**G06G 7/14**

IPC 8 full level

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CPC (source: EP)

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