

Title (en)
WAFER-LEVEL SEAL FOR NON-SILICON-BASED DEVICES

Title (de)
VERSIEGELUNG AUF WAFEREBENE FÜR NICHT AUF SILIZIUM BASIERENDE BAUELEMENTE

Title (fr)
JOINT SITUE AU NIVEAU PLAQUETTE POUR DISPOSITIFS EXEMPTS DE SILICIUM

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Abstract (en)
[origin: WO2004021398A2] One embodiment disclosed relates to a method (100) for sealing an active area of a non-silicon-based device on a wafer. The method includes providing (104) a sacrificial material over at least the active area of the non-silicon-based device, depositing (108) a seal coating over the wafer so that the seal coating covers the sacrificial material, and replacing (112, 114) the sacrificial material with a target atmosphere. Another embodiment disclosed relates to an SAW device sealed at the wafer level (i.e. prior to separation of the die from the wafer). The device includes an active area to be protected, an electrical contact area (4), and a lithographically-formed structure (24) sealing at least the active area and leaving at least a portion of the electrical contact area (4) exposed.

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