

Title (en)

METHOD FOR SYNCHRONIZING EVENTS, PARTICULARLY FOR PROCESSORS OF FAULT-TOLERANT SYSTEMS

Title (de)

VERFAHREN ZUR EREIGNISSYNCHRONISATION, DAMIT SYNCHRONISIERTE PROZESSOREN UND FEHLERTOLERANTER SYSTEME MIT DERARTIGEN PROZESSOREN

Title (fr)

PROCEDE DE SYNCHRONISATION D'EVENEMENTS, NOTAMMENT POUR DES PROCESSEURS DE SYSTEMES A TOLERANCE D'ERREUR

Publication

**EP 1543421 A2 20050622 (DE)**

Application

**EP 03807779 A 20030806**

Priority

- EP 03807779 A 20030806
- EP 0308715 W 20030806
- EP 02020602 A 20020912

Abstract (en)

[origin: EP1398699A1] Method for synchronization of external events that are fed to a CPU in order to affect its behavior. Accordingly the events are temporarily stored and then fed to the CPU for processing when called up by its execution unit (EU). This operating mode is entered when a condition, previously set by a command or fixed operating condition, is fulfilled. Independent claims are also made for: (a) a CPU that comprises: at least a program execution unit (EU), at least a counter (CIC), at least a register (MIR) and at least a comparator (K) for switching the execution unit into a special operating mode when the counter unit matches the register unit; (b) and a system comprising at least two such CPUs.

IPC 1-7

**G06F 11/16**

IPC 8 full level

**G06F 11/16** (2006.01)

CPC (source: EP US)

**G06F 11/1683** (2013.01 - EP US)

Citation (search report)

See references of WO 2004034172A2

Designated contracting state (EPC)

DE FR GB SE

DOCDB simple family (publication)

**EP 1398699 A1 20040317**; AU 2003260375 A1 20040504; AU 2003260375 A8 20040504; CA 2498656 A1 20040422; CN 1682194 A 20051012; EP 1543421 A2 20050622; US 2006195849 A1 20060831; WO 2004034172 A2 20040422; WO 2004034172 A3 20040923

DOCDB simple family (application)

**EP 02020602 A 20020912**; AU 2003260375 A 20030806; CA 2498656 A 20030806; CN 03821546 A 20030806; EP 0308715 W 20030806; EP 03807779 A 20030806; US 52742805 A 20051102