

Title (en)

SYSTEM FOR CONTROLLING MODE CHANGES IN A VOLTAGE DOWN-CONVERTER

Title (de)

SYSTEM ZUR STEUERUNG VON MODUSWECHSELN IN EINEM SPANNUNGSABWÄRTSUMSETZER

Title (fr)

SYSTEME DE COMMANDE DES CHANGEMENTS DE MODE DANS UN CONVERTISSEUR ABAISSEUR DE TENSION

Publication

EP 1547088 A4 20070502 (EN)

Application

EP 03754491 A 20030910

Priority

- IT TO20020794 A 20020912
- US 0328441 W 20030910
- US 40764603 A 20030403

Abstract (en)

[origin: WO2004025657A1] A voltage down-converter system, with a stand-by mode and an active mode, for a memory device with the following components. A charge node (68) is configured to receive a charge. A first transistor (54) has a first gate and the first transistor is configured to supply a load current to the memory device. A first switch (58) is coupled to the charge node and the first gate, the first switch being configured to apply the charge in the charge node to the first gate during transition from stand-by to active modes. A second transistor (66) is coupled to the first gate and configured to bias the first transistor to an inactive state during stand-by mode. A second switch (64) is coupled to the first gate and the second transistor, the second switch being configured to apply a voltage difference at the second transistor to the first gate during the stand-by mode.

IPC 1-7

G11C 7/00; G05F 1/10; G05F 3/02; G05F 3/16; G05F 3/20

IPC 8 full level

G11C 11/413 (2006.01); **G05F 1/56** (2006.01); **G11C 5/14** (2006.01)

CPC (source: EP KR)

G11C 5/147 (2013.01 - EP); **G11C 7/00** (2013.01 - KR)

Citation (search report)

- [X] US 6292015 B1 20010918 - OOISHI TSUKASA [JP], et al
- [Y] EP 0454170 A2 19911030 - NEC CORP [JP]
- [Y] KENICHI IMAMIYA ET AL: "A 130-mm, 256-Mbit NAND Flash with Shallow Trench Isolation Technology", IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE SERVICE CENTER, PISCATAWAY, NJ, US, vol. 34, no. 11, November 1999 (1999-11-01), XP011061107, ISSN: 0018-9200
- See references of WO 2004025657A1

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