

Title (en)

Driving circuit of an liquid crystal display and its driving method

Title (de)

Steuerschaltung einer Flüssigkristallanzeige und Steuerverfahren dafür

Title (fr)

Circuit de commande d'un dispositif d'affichage à cristaux liquides et sa méthode de commande

Publication

**EP 1548698 A1 20050629 (EN)**

Application

**EP 03029643 A 20031222**

Priority

EP 03029643 A 20031222

Abstract (en)

A method for driving a liquid crystal display (LCD) panel includes receiving continuously a plurality of frame data, generating a plurality of data impulses (GN,GN(2)) for each pixel every frame period according to the frame data, and applying the plurality of data impulses to a pixel of a liquid crystal device within a frame period via the data line connected to the pixel in order to control a transmission rate of the liquid crystal device. A processing circuit (42) a plurality of overdriven pixel data (GN) according to the current pixel data (Gm) and delayed pixel data (Gm-1). Stored in a first memory (44). A second memory (46) outputs the plurality of overdriven pixel data (GN,GN(2)) to each pixel. <IMAGE>

IPC 1-7

**G09G 3/36**

IPC 8 full level

**G09G 3/20** (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP)

**G09G 3/2011** (2013.01); **G09G 3/3648** (2013.01); **G09G 3/2025** (2013.01); **G09G 3/2081** (2013.01); **G09G 2320/0252** (2013.01); **G09G 2320/0261** (2013.01); **G09G 2340/16** (2013.01)

Citation (search report)

- [X] US 2002044115 A1 20020418 - JINDA AKIHITO [JP], et al
- [X] US 2003058264 A1 20030327 - TAKAKO ADACHI [JP], et al
- [A] EP 0660297 A2 19950628 - SHARP KK [JP]
- [Y] EP 1122711 A2 20010808 - SAMSUNG ELECTRONICS CO LTD [KR]
- [Y] EP 0539185 A1 19930428 - SHARP KK [JP]
- [A] LEE B ET AL: "REDUCING GRAY-LEVEL RESPONSE TO ONE FRAME: DYNAMIC CAPACITANCE COMPENSATION", 2001 SID INTERNATIONAL SYMPOSIUM DIGEST OF TECHNICAL PAPERS. SAN JOSE, CA, JUNE 5 - 7, 2001, SID INTERNATIONAL SYMPOSIUM DIGEST OF TECHNICAL PAPERS, SAN JOSE, CA: SID, US, vol. 32, June 2001 (2001-06-01), pages 1260 - 1263, XP001054131

Citation (examination)

JP H0662355 A 19940304 - MATSUSHITA ELECTRIC IND CO LTD

Cited by

EP1669975A3

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

**EP 1548698 A1 20050629**

DOCDB simple family (application)

**EP 03029643 A 20031222**