

Title (en)  
PROGRAMMABLE LOGIC DEVICE

Title (de)  
EINRICHTUNG DER PROGRAMMIERBAREN LOGIK

Title (fr)  
DISPOSITIF LOGIQUE PROGRAMMABLE

Publication  
**EP 1554805 A2 20050720 (DE)**

Application  
**EP 03773569 A 20031023**

Priority  
• DE 0303524 W 20031023  
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Abstract (en)  
[origin: WO2004040766A2] The invention relates to a programmable logic device (7) comprising several logic blocks (3A to 3D) with configurable characteristics, elements for linking the logic blocks to one another and a processing unit (4) and an input/output unit (5). In addition, the linking elements have at least one configurable changeover logic block, enabling the logic blocks (3A to 3D) to be re-configured during the operation of the logic device (7). Said changeover logic block is used for the configuration of at least one of the re-configurable logic blocks (3A to 3D) and its connection to other blocks and/or its connection to the processing unit (4) and/or to the input/output unit (5).

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IPC 8 full level  
**H03K 19/177** (2006.01)

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**H03K 19/17736** (2013.01 - EP US); **H03K 19/17752** (2013.01 - EP US); **H03K 19/17756** (2013.01 - EP US); **H03K 19/17796** (2013.01 - EP US)

Citation (search report)  
See references of WO 2004040766A2

Citation (examination)  
US 6255848 B1 20010703 - SCHULTZ DAVID P [US], et al

Designated contracting state (EPC)  
DE FR GB NL

DOCDB simple family (publication)  
**WO 2004040766 A2 20040513; WO 2004040766 A3 20041028**; DE 10347975 A1 20040513; DE 10347975 B4 20081009;  
EP 1554805 A2 20050720; JP 2006504344 A 20060202; JP 4160956 B2 20081008; US 2006055421 A1 20060316; US 7161383 B2 20070109

DOCDB simple family (application)  
**DE 0303524 W 20031023**; DE 10347975 A 20031015; EP 03773569 A 20031023; JP 2004547411 A 20031023; US 53264305 A 20050425