

Title (en)

METHOD AND DEVICE FOR SYNTHESISING AN ELECTRICAL ARCHITECTURE

Title (de)

VERFAHREN UND GERÄT ZUM SYNTHETISIEREN EINER ELEKTRISCHEN ARCHITEKTUR

Title (fr)

PROCEDE ET DISPOSITIF POUR SYNTHETISER UNE ARCHITECTURE ELECTRIQUE

Publication

**EP 1556803 A2 20050727 (FR)**

Application

**EP 03778417 A 20031021**

Priority

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Abstract (en)

[origin: FR2846117A1] The method involves representing two dimensional geometry of a vehicle cut into two sections. Routing points are placed in the sections to route electrical wires, and connection points are placed between the two sections. Routing synthesis and evaluation are iteratively performed based on the geometry, position of the points and electronic components. The positions are modified based on the evaluation results, and routing synthesis and evaluation steps are repeated. An Independent claim is also included for a device for synthesizing electric or electronic architecture of a part of a product.

IPC 1-7

**G06F 17/50**

IPC 8 full level

**G06F 17/50** (2006.01)

CPC (source: EP KR US)

**G06F 17/00** (2013.01 - KR); **G06F 30/18** (2020.01 - EP US); **G06Q 30/0283** (2013.01 - EP US)

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