

Title (en)

METHOD AND DEVICE FOR SYNTHESISING AN ELECTRICAL ARCHITECTURE

Title (de)

VERFAHREN UND GERÄT ZUM SYNTHEZISIEREN EINER ELEKTRISCHEN ARCHITEKTUR

Title (fr)

PROCEDE ET DISPOSITIF POUR SYNTETISER UNE ARCHITECTURE ELECTRIQUE

Publication

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Application

EP 03778417 A 20031021

Priority

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Abstract (en)

[origin: FR2846117A1] The method involves representing two dimensional geometry of a vehicle cut into two sections. Routing points are placed in the sections to route electrical wires, and connection points are placed between the two sections. Routing synthesis and evaluation are iteratively performed based on the geometry, position of the points and electronic components. The positions are modified based on the evaluation results, and routing synthesis and evaluation steps are repeated. An Independent claim is also included for a device for synthesizing electric or electronic architecture of a part of a product.

IPC 1-7

G06F 17/50

IPC 8 full level

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CPC (source: EP KR US)

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JP 2006504169 A 20060202; JP 2006504170 A 20060202; JP 2006504171 A 20060202; KR 101002372 B1 20101217;
KR 101004302 B1 20101228; KR 101010975 B1 20110126; KR 20050074520 A 20050718; KR 20050074521 A 20050718;
KR 20050086425 A 20050830; US 2006143587 A1 20060629; US 2006215825 A1 20060928; US 2006229742 A1 20061012;
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