

Title (en)

A MULTI-RATE, MULTI-PORT, GIGABIT SERDES TRANSCEIVER

Title (de)

MEHRRATEN-, MEHRPORT-, GIGABIT-SERDES-SENDER-/EMPFÄNGER

Title (fr)

EMETTEUR-RECEPTEUR SERDES GIGABIT A VITESSES MULTIPLES ET PORTS MULTIPLES

Publication

EP 1558987 A4 20100623 (EN)

Application

EP 03779391 A 20031029

Priority

- US 0334234 W 20031029
- US 42178002 P 20021029

Abstract (en)

[origin: US2004083077A1] An integrated packet bit error rate tester includes a packet transmit circuit that has a first memory for storing transmit packet data and is connectable to a channel under test. A packet receive circuit includes a second memory for storing received packet data and is connectable to the channel under test. An interface is used for programming the packet transmit and packet receive circuits. The packet transmit circuit can generate an arbitrary 10G SERDES packet in response to commands from the interface. The packet receive circuit can determine a bit error rate of the channel under test. The second memory can capture received packet data upon any one of (a) after a pre-programmed pattern is detected, (b) after a pre-programmed pattern is lost, and (c) after an error is detected.

IPC 8 full level

G06F 3/00 (2006.01); **G01R 31/08** (2006.01); **G01R 31/28** (2006.01); **G06F 13/40** (2006.01); **G21C 17/00** (2006.01); **H04J 1/16** (2006.01); **H04J 3/04** (2006.01); **H04J 3/14** (2006.01); **H04L 1/00** (2006.01); **H04L 12/26** (2006.01); **H04L 12/40** (2006.01); **H04Q 11/00** (2006.01)

IPC 8 main group level

H04L (2006.01)

CPC (source: EP US)

G06F 13/4027 (2013.01 - EP US); **H04L 43/50** (2013.01 - EP US); **Y04S 40/00** (2013.01 - EP US)

Citation (search report)

- [XYI] US 4833605 A 19890523 - TERADA HIROAKI [JP], et al
- [Y] US 4821174 A 19890411 - WEBB RICHARD F [US], et al
- [Y] US 5751699 A 19980512 - RADKE WILLIAM H [US]
- See references of WO 2004040824A2

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

US 2004083077 A1 20040429; **US 7373561 B2 20080513**; EP 1558987 A2 20050803; EP 1558987 A4 20100623; EP 1558987 B1 20130911; EP 2592529 A1 20130515; US 2004088443 A1 20040506; US 2004088444 A1 20040506; US 2004117698 A1 20040617; US 2004141497 A1 20040722; US 2004141531 A1 20040722; US 2006250985 A1 20061109; US 2008186987 A1 20080807; US 2009041060 A1 20090212; US 2009252160 A1 20091008; US 2010100651 A1 20100422; US 2012072615 A1 20120322; US 2012239846 A1 20120920; US 7035228 B2 20060425; US 7355987 B2 20080408; US 7450529 B2 20081111; US 7450530 B2 20081111; US 7533311 B2 20090512; US 7664888 B2 20100216; US 8001286 B2 20110816; US 8023436 B2 20110920; US 8086762 B2 20111227; US 8094590 B2 20120110; US 9330043 B2 20160503; WO 2004040824 A2 20040513; WO 2004040824 A3 20050210

DOCDB simple family (application)

US 68124403 A 20031009; EP 03779391 A 20031029; EP 13000556 A 20031029; US 0334234 W 20031029; US 201113229150 A 20110909; US 201113305207 A 20111128; US 25385108 A 20081017; US 38950606 A 20060327; US 41664109 A 20090401; US 65434509 A 20091217; US 69472903 A 20031029; US 69473003 A 20031029; US 69478803 A 20031029; US 69545803 A 20031029; US 69549803 A 20031029; US 7878008 A 20080404