

Title (en)
PLL WITH BALANCED QUADRICORRELATOR

Title (de)
PLL MIT SYMMETRISCHEM QUADRIKORRELATOR

Title (fr)
PLL MUNIE D'UN QUADRI-CORRELATEUR EQUILIBRE

Publication
EP 1563605 B1 20070110 (EN)

Application
EP 03810547 A 20031008

Priority
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• IB 0304467 W 20031008

Abstract (en)
[origin: WO2004042925A1] A Phase Locked Loop (1) comprising a frequency detector (10) including a balanced quadricorrelator (2), the loop (1) being characterized in that the quadricorrelator (2) comprises double edge clocked bi-stable circuits (21, 22, 23, 24, 25, 26, 27, 28) coupled to multiplexers (31, 32, 33, 34) being controlled by a signal having the same bitrate as the incoming D signal (D).

IPC 8 full level
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