

Title (en)

DEVICE AND METHOD FOR ANALYSING EMBEDDED SYSTEMS

Title (de)

EINRICHTUNG UND VERFAHREN ZUR ANALYSE VON EINGEBETTETEN SYSTEMEN

Title (fr)

PROCEDE ET DISPOSITIF POUR ANALYSER DES SYSTEMES ENCASTRES

Publication

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Application

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Priority

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Abstract (en)

[origin: WO2004049159A2] The invention relates to an analysis device for an embedded system (9) comprising a CPU (1), a CPU bus (2) and a memory (3). The embedded system has at least one communication module (4) for the input or output of analysis data via a test interface (5). The communication module permits the internal memory and the input and output access operations of the embedded system to be monitored and/or logged without using the clock cycles of the CPU (1).

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