

Title (en)  
CHIP LEVEL PHASE ADJUSTMENT

Title (de)  
PHASENEINSTELLUNG AUF CHIPEBENE

Title (fr)  
REGLAGE DE PHASE AU NIVEAU CHIP

Publication  
**EP 1565994 A1 20050824 (EN)**

Application  
**EP 03774851 A 20031015**

Priority  
• US 0332757 W 20031015  
• US 41818802 P 20021015

Abstract (en)  
[origin: WO2004036783A1] In a CDMA on a WCDMA system, the phase correction is performed at a chip level ie. Before the Walsh and the user-PN despreading. The correction is carried out performing the dot product between the despreaded pilot signals and the received data signal not despreaded.

IPC 1-7  
**H04B 1/707**

IPC 8 full level  
**H04B 1/707** (2006.01)

CPC (source: EP)  
**H04B 1/7107** (2013.01)

Citation (search report)  
See references of WO 2004036783A1

Designated contracting state (EPC)  
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DOCDB simple family (publication)  
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**US 0332757 W 20031015**; AU 2003282858 A 20031015; CN 200380105881 A 20031015; EP 03774851 A 20031015; JP 2004545348 A 20031015