

Title (en)
SYSTEM AND METHOD FOR TRUE RANDOM NUMBER GENERATION

Title (de)
SYSTEM UND VERFAHREN ZUR ECHTEN ZUFALLSZAHLENERZEUGUNG

Title (fr)
SYSTEME ET PROCEDE DE GENERATION DE NOMBRES ALEATOIRES PURS

Publication
EP 1573509 A2 20050914 (EN)

Application
EP 03772477 A 20031118

Priority
• IB 0305265 W 20031118
• US 43134102 P 20021205

Abstract (en)
[origin: WO2004051458A2] There is provided a system and method for providing an entirely digital and/or digitally synthesizable true random number generator for incorporation on integrated circuits using any standard logic synthesis tool or comparable technique. The system and method has at least a microprocessor operating at a first frequency, at least one counter for generating bits, at least one shifter for scrambling bits, at least one first oscillator for cooperating with the at least one counter; and at least one second oscillator for cooperating with the at least one shifter. The system being configured to provide a frequency perturbation based on digital input signals initialized via the microprocessor.

IPC 1-7
G06F 7/58

IPC 8 full level
G06F 7/58 (2006.01)

CPC (source: EP KR)
G06F 7/58 (2013.01 - KR); **G06F 7/588** (2013.01 - EP)

Citation (search report)
See references of WO 2004051458A2

Citation (examination)
US 4855690 A 19890808 - DIAS DONALD R [US]

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LU MC NL PT RO SE SI SK TR

DOCDB simple family (publication)
WO 2004051458 A2 20040617; WO 2004051458 A3 20041202; AU 2003280091 A1 20040623; CN 1720501 A 20060111;
EP 1573509 A2 20050914; JP 2006509286 A 20060316; KR 20050084153 A 20050826

DOCDB simple family (application)
IB 0305265 W 20031118; AU 2003280091 A 20031118; CN 200380105269 A 20031118; EP 03772477 A 20031118; JP 2004556611 A 20031118;
KR 20057010124 A 20050603