

Title (en)  
SYSTEM AND METHOD FOR TRUE RANDOM NUMBER GENERATION

Title (de)  
SYSTEM UND VERFAHREN ZUR ECHTEN ZUFALLSZAHLENERZEUGUNG

Title (fr)  
SYSTEME ET PROCEDE DE GENERATION DE NOMBRES ALEATOIRES PURS

Publication  
**EP 1573509 A2 20050914 (EN)**

Application  
**EP 03772477 A 20031118**

Priority  
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Abstract (en)  
[origin: WO2004051458A2] There is provided a system and method for providing an entirely digital and/or digitally synthesizable true random number generator for incorporation on integrated circuits using any standard logic synthesis tool or comparable technique. The system and method has at least a microprocessor operating at a first frequency, at least one counter for generating bits, at least one shifter for scrambling bits, at least one first oscillator for cooperating with the at least one counter; and at least one second oscillator for cooperating with the at least one shifter. The system being configured to provide a frequency perturbation based on digital input signals initialized via the microprocessor.

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**G06F 7/58**

IPC 8 full level  
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CPC (source: EP KR)  
**G06F 7/58** (2013.01 - KR); **G06F 7/588** (2013.01 - EP)

Citation (search report)  
See references of WO 2004051458A2

Citation (examination)  
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