

Title (en)

DATAFLOW-SYNCHRONIZED EMBEDDED FIELD PROGRAMMABLE PROCESSOR ARRAY

Title (de)

DATENFLUSSSYNCHRONISIERTES EINGEBETTETES, AM EINSATZORT PROGRAMMIERBARES PROZESSORARRAY

Title (fr)

RESEAU DE PROCESSEUR PROGRAMMABLE PAR L'UTILISATEUR INTEGRE SYNCHRONISE PAR LE FLUX DE DONNEES

Publication

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Application

EP 03775666 A 20031128

Priority

- IB 0305623 W 20031128
- US 43297102 P 20021212
- US 47516603 P 20030602

Abstract (en)

[origin: WO2004053716A2] An embedded field programmable processor includes a two-dimensional array of processing cells for performing mathematical operations whose timing depends on the inflow of operands. An array interface reconfigurably connects paths for the inflow to respective cells on the array periphery. The array is preferably of the systolic type and is preferably implemented with nearest neighbor inter-cell connections.

IPC 1-7

G06F 15/80

IPC 8 full level

G06F 15/80 (2006.01)

CPC (source: EP KR)

G06F 15/80 (2013.01 - KR); **G06F 15/8007** (2013.01 - EP); **G06F 15/8023** (2013.01 - EP)

Citation (search report)

See references of WO 2004053716A2

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