

Title (en)  
IMAGE-SENSOR ARCHITECTURE FOR PER-PIXEL CHARGE-INTEGRATION CONTROL

Title (de)  
ARCHITEKTUR EINES BILDSSENSORS ZUR KONTROLLE VON LADUNGSINTEGRATION PRO PIXEL

Title (fr)  
ARCHITECTURE DE CAPTEUR D'IMAGE ASSURANT LA COMMANDE D'INTEGRATION DE CHARGE PAR PIXEL

Publication  
**EP 1574036 A2 20050914 (EN)**

Application  
**EP 00949865 A 20000724**

Priority  
• IL 0000438 W 20000724  
• US 14596099 P 19990729  
• US 51616800 A 20000229

Abstract (en)  
[origin: WO0110110A2] A sensor array that produces a captured image at the end of a time frame. The array includes a plurality of unit cells which sense the image with multiple within-frame charge-integrations, and control means which separately controls each of the unit cells. The unit cells are programmable multiple charge-integration unit cells with modes of photocurrent integration and non-integration. The control means includes means for separately controlling multiple charge-integrations in a single frame capture of each unit cell, independently of the charge-integrations of the other unit cells.

IPC 1-7  
**H04N 1/00**

IPC 8 full level  
**H01L 27/146** (2006.01); **H04N 3/14** (2006.01); **H04N 5/228** (2006.01); **H04N 5/353** (2011.01); **H04N 5/355** (2011.01)

CPC (source: EP)  
**H04N 25/533** (2023.01); **H04N 25/57** (2023.01); **H04N 25/583** (2023.01)

Designated contracting state (EPC)  
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)  
**WO 0110110 A2 20010208**; **WO 0110110 A3 20051222**; AU 6311900 A 20010219; EP 1574036 A2 20050914; JP 2003527775 A 20030916

DOCDB simple family (application)  
**IL 0000438 W 20000724**; AU 6311900 A 20000724; EP 00949865 A 20000724; JP 2001513879 A 20000724