

Title (en)
DIGITAL GROUP DELAY COMPENSATOR

Title (de)
DIGITALES KOMPENSATIONSGLIED FÜR GRUPPENVERZÖGERUNG

Title (fr)
COMPENSATEUR DE PHASE NUMERIQUE

Publication
EP 1576753 A4 20090513 (EN)

Application
EP 03773121 A 20031006

Priority
• US 0331416 W 20031006
• US 41670502 P 20021007
• US 67762103 A 20031002

Abstract (en)
[origin: WO2004034624A2] A digital group delay compensation system comprising a digital allpass filter that is utilized in an implementation phase; and a system that generates coefficients for the allpass filter used in the implementation phase such that the overall performance of a system is measured and optimized in a calibration phase.

IPC 8 full level
G06F 17/10 (2006.01); **H03H 17/00** (2006.01); **H04L 1/00** (2006.01); **H04L 25/03** (2006.01)

IPC 8 main group level
H04L (2006.01)

CPC (source: EP)
H03H 17/0009 (2013.01); **H04L 25/03885** (2013.01)

Citation (search report)
• [A] US 4816767 A 19890328 - CANNON WAYNE C [US], et al
• See references of WO 2004034624A2

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LU MC NL PT RO SE SI SK TR

DOCDB simple family (publication)
WO 2004034624 A2 20040422; **WO 2004034624 A3 20060406**; EP 1576753 A2 20050921; EP 1576753 A4 20090513; JP 2006511987 A 20060406

DOCDB simple family (application)
US 0331416 W 20031006; EP 03773121 A 20031006; JP 2004543138 A 20031006