

Title (en)
METHOD FOR PRODUCING BIT LINES FOR UCP FLASH MEMORIES

Title (de)
VERFAHREN ZUM HERSTELLEN VON BITLEITUNGEN FÜR UCP-FLASH-SPEICHER

Title (fr)
PROCEDE POUR PRODUIRE DES LIGNES DE BINAIRES DESTINEES A DES MEMOIRES FLASH UCP

Publication
EP 1588417 A2 20051026 (DE)

Application
EP 04702285 A 20040115

Priority
• DE 2004000042 W 20040115
• DE 10303847 A 20030130

Abstract (en)
[origin: US2006024889A1] A semiconductor device can be fabricated by forming a floating gate layer over a semiconductor body. The floating gate layer is at least partially arranged over an insulation region in the semiconductor body. The floating gate layer is patterned to expose a portion of the insulation region. A recess is formed in a portion of the insulation region exposed by the patterned floating gate layer. A conductor is deposited within the recess. The conductor serves as a buried bitline. An insulator can then be formed within the recess over the conductor.

IPC 1-7
H01L 21/8247

IPC 8 full level
H01L 21/336 (2006.01); **H01L 21/8247** (2006.01); **H01L 29/788** (2006.01); **H10B 69/00** (2023.01)

CPC (source: EP US)
H10B 41/30 (2023.02 - EP US); **H10B 69/00** (2023.02 - EP US)

Citation (examination)
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Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
US 2006024889 A1 20060202; **US 7485542 B2 20090203**; CN 1745473 A 20060308; CN 1745473 B 20100414; EP 1588417 A2 20051026; WO 2004068578 A2 20040812; WO 2004068578 A3 20041028

DOCDB simple family (application)
US 19405905 A 20050729; CN 200480003230 A 20040115; DE 2004000042 W 20040115; EP 04702285 A 20040115