

Title (en)

METHOD FOR USING A MICROPROCESSOR AND A MICROPROCESSOR SYSTEM

Title (de)

VERFAHREN ZUM BETREIBEN EINES MIKROPROZESSORS UND EINE MIKROPROZESSORANORDNUNG

Title (fr)

PROCEDE D'EXPLOITATION D'UN MICROPROCESSEUR ET SYSTEME DE MICROPROCESSEUR

Publication

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Application

EP 04709578 A 20040210

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Abstract (en)

[origin: WO2004081971A2] The invention relates to a method for using a microprocessor consisting of at least one program branching and/or program delay which are regulated by random bits in order to modulate a program flow, implemented and stored in the form of a material command. The inventive method is characterised in that a program runtime for each program run is different each time with respect to the runtime of the previous programs. A microprocessor system for carrying out said method is also disclosed.

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G06F 1/00

IPC 8 full level

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CPC (source: EP US)

G06F 21/755 (2017.07 - EP US)

Citation (search report)

See references of WO 2004081971A2

Citation (examination)

- WO 9963696 A1 19991209 - CRYPTOGRAPHY RES INC [US], et al
- J IRWIN ET AL: "Instruction stream mutation for non-deterministic processors", APPLICATION-SPECIFIC SYSTEMS, ARCHITECTURES AND PROCESSORS, 2002. PROC EEDINGS. THE IEEE INTERNATIONAL CONFERENCE ON 17-19 JULY 2002, 1 December 2001 (2001-12-01), XP055263776, ISBN: 978-0-7695-1712-4, Retrieved from the Internet <URL:http://www.cs.bris.ac.uk/Publications/Papers/1000604.pdf> [retrieved on 20160408]

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