

Title (en)

Verification of a bit stream

Title (de)

Verifikation eines Bitstromes

Title (fr)

Vérification d'un flux de bits

Publication

EP 1610463 A1 20051228 (FR)

Application

EP 05105576 A 20050622

Priority

FR 0451325 A 20040624

Abstract (en)

The generator has normalization circuits (3, 4) receiving a bit stream to provide normalized bit streams, and registers (6, 7) storing the normalized bit streams. A detector (8) matches the normalized bit streams and compares the bit streams with one of four possible pairs of bits (00, 01, 10, 11) to increment a value of a counter (9). A block (5) compares the values of the counter to verify equi-distribution of the pairs of bits. An independent claim is also included for a method of detecting a loss of equiprobable character of an output bit stream.

Abstract (fr)

L'invention concerne un procédé et un circuit de détection d'une éventuelle perte de caractère équiprobable d'un premier flux de bits de sortie (NBS1) issu d'au moins un premier élément de normalisation (3) d'un flux de bits initial (BS), consistant à soumettre le flux initial à au moins un deuxième élément de normalisation (4) de nature différente du premier, apparier (8), bit à bit, les flux issus des deux éléments, et vérifier (5, 9) l'équirépartition des paires d'états différents. <IMAGE>

IPC 1-7

H03K 3/84

IPC 8 full level

H03K 3/84 (2006.01)

CPC (source: EP US)

H03K 3/84 (2013.01 - EP US)

Citation (search report)

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