

Title (en)

SYSTEMS AND METHODS FOR TASK MIGRATION

Title (de)

SYSTEME UND VERFAHREN ZUR TASK-MIGRATION

Title (fr)

SYSTÈMES ET PROCÉDÉS PERMETTANT LA MIGRATION DE TACHES

Publication

EP 1620800 B1 20190327 (EN)

Application

EP 05738845 A 20050428

Priority

- JP 2005008574 W 20050428
- US 83805004 A 20040503

Abstract (en)

[origin: US2005251667A1] Methods and systems are provided whereby, in one aspect, pointers to address locations of instructions, static data and dynamically-created data are stored such that the instructions, static data and dynamically-created data can be moved to a different memory or processor without changing the values of the pointers.

IPC 8 full level

G06F 9/32 (2006.01); **G06F 9/46** (2006.01); **G06F 9/42** (2006.01); **G06F 9/44** (2006.01); **G06F 9/48** (2006.01); **G06F 9/50** (2006.01); **G06F 15/167** (2006.01)

CPC (source: EP US)

G06F 9/4486 (2018.01 - EP US); **G06F 9/4812** (2013.01 - EP US)

Citation (examination)

- US 6625718 B1 20030923 - STEINER ROBERT C [US]
- US 6345276 B1 20020205 - LEE HENRY [US]
- SWAN R J ET AL: "Cm-A modular, multi-microprocessor", NATIONAL COMPUTER CONFERENCE,, 1 January 1977 (1977-01-01), pages 637 - 644, XP001608954

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU MC NL PL PT RO SE SI SK TR

DOCDB simple family (publication)

US 2005251667 A1 20051110; **US 7437536 B2 20081014**; EP 1620800 A1 20060201; EP 1620800 A4 20110928; EP 1620800 B1 20190327; JP 2005322232 A 20051117; JP 4015159 B2 20071128; WO 2005106662 A1 20051110

DOCDB simple family (application)

US 83805004 A 20040503; EP 05738845 A 20050428; JP 2005008574 W 20050428; JP 2005130463 A 20050427