

Title (en)  
Display device driving circuit

Title (de)  
Anzeigesteuerschaltung

Title (fr)  
Circuit de commande d'affichage

Publication  
**EP 1622123 A2 20060201 (EN)**

Application  
**EP 05300521 A 20050628**

Priority  
• EP 04017851 A 20040728  
• EP 05300521 A 20050628

Abstract (en)  
A driving circuit for a display with display elements in rows and/or columns has a shift register, through which tokens are shifted. The shift register's parallel outputs are latched and enable switch cells depending on the tokens. Control signals are supplied to the switch cells which control the output signal in terms of pulse width and/or signal shape. Buffers output the signals to a connected display. Individual or groups of buffers are connected to different supply voltages. The shift register may have more than one input in order to allow for shifting tokens in parallel, e.g. to every second output, using only one clock cycle. Further, inputs are provided for inverting the travelling direction of the tokens, inverting the shape of the signal that is output or switching all outputs to a predetermined state.

IPC 8 full level  
**G09G 3/20** (2006.01); **G09G 3/32** (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP)  
**G09G 3/20** (2013.01); **G09G 3/325** (2013.01); **G09G 3/3266** (2013.01); **G09G 2300/0842** (2013.01); **G09G 2300/0861** (2013.01); **G09G 2310/0205** (2013.01); **G09G 2310/0224** (2013.01); **G09G 2310/0254** (2013.01); **G09G 2310/0267** (2013.01); **G09G 2310/0283** (2013.01); **G09G 2330/021** (2013.01); **G09G 2330/028** (2013.01)

Designated contracting state (EPC)  
DE FR GB

Designated extension state (EPC)  
AL BA HR LV MK YU

DOCDB simple family (publication)  
**EP 1622123 A2 20060201**; **EP 1622123 A3 20090506**; **EP 1622123 B1 20141022**

DOCDB simple family (application)  
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