

Title (en)

Power amplifier arrangement and method for memory correction/linearization

Title (de)

Leistungsverstärkervorrichtung und Verfahren zur Korrektur/Linearisierung mit einem Speicher

Title (fr)

Dispositif amplificateur de puissance et procédé pour la correction/linéarisation avec une mémoire

Publication

EP 1622256 B1 20101117 (EN)

Application

EP 05076668 A 20050720

Priority

US 90030004 A 20040728

Abstract (en)

[origin: EP1622256A1] A device, system, and method are provided for a power amplifier arrangement. In embodiments of a Vdd modulated power amplifier arrangement, a memory correction block includes a Vdd predictor for predicting waveform distortions of a Vdd modulated power supply and a main-path corrector for pre-distorting the input to the power amplifier arrangement as a function of the input to the power amplifier arrangement, an input envelope signal determined as a function of the input to the power amplifier arrangement, and an output of the Vdd predictor. Embodiments of the invention provide for operating power amplifier arrangements having memory. In some embodiments, methods of training the Vdd predictor and the main-path corrector of the memory correction block are provided.

IPC 8 full level

H03F 1/32 (2006.01)

CPC (source: EP US)

H03F 1/0211 (2013.01 - EP US); **H03F 1/3258** (2013.01 - EP US); **H03F 2200/102** (2013.01 - EP US)

Cited by

CN104704739A; KR20150065853A; US9112649B2; WO2014059311A1; WO2019190669A1; WO2014120612A1

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