

Title (en)

METHOD AND APPARATUS FOR SMOOTHING CURRENT CONSUMPTION IN AN INTEGRATED CIRCUIT

Title (de)

VERFAHREN UND VORRICHTUNG ZUM GLÄTTEN DES STROMVERBRAUCHS IN EINER INTEGRIERTEN SCHALTUNG

Title (fr)

METHODE ET APPAREIL POUR LISSER UNE CONSOMMATION DE COURANT DANS UN CIRCUIT INTEGRE

Publication

**EP 1651969 A4 20070307 (EN)**

Application

**EP 04778231 A 20040712**

Priority

- US 2004022626 W 20040712
- FR 0308735 A 20030717
- US 88320304 A 20040630

Abstract (en)

[origin: WO2005010939A2] The invention includes a system for smoothing current consumption in a digital logic module. A processing circuit is configured to receive an input and manipulate the input during a processing time period to produce an output. A non-processing time period follows production of the output. The processing circuit is further configured to draw an active range of current during the processing time period and an inactive range of current during the non-processing time period. The system comprises a smoothing circuit coupled to the processing circuit and configured to engage the processing circuit during the non-processing time period. The smoothing circuit is also configured to cause the processing circuit to draw an active range of current during the non-processing time period.

IPC 8 full level

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CPC (source: EP US)

**G06F 21/755** (2017.07 - EP US)

Citation (search report)

- [X] US 5995629 A 19991130 - REINER ROBERT [DE]
- [X] WO 0023866 A1 20000427 - GEMPLUS CARD INT [FR], et al
- See references of WO 2005010939A2

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DE FR GB

DOCDB simple family (publication)

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