

Title (en)

HUB COMPONENT FOR CONNECTION TO ONE OR MORE MEMORY MODULES

Title (de)

HUB-BAUSTEIN ZUM ANSCHLIESSEN VON EINEM ODER MEHREREN SPEICH ERBAUSTEINEN

Title (fr)

CONCENTRATEUR POUR LE RACCORDEMENT D'UN OU DE PLUSIEURS MODULES DE MEMOIRE

Publication

EP 1652190 A1 20060503 (DE)

Application

EP 04763824 A 20040805

Priority

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- DE 10335978 A 20030806

Abstract (en)

[origin: WO2005017903A1] The invention relates to a hub component for connection to one or more memory modules via a respective memory module interface. Said hub component comprises an address input for connection of the hub component to an address bus, and an address output for connection to a second address bus, an address coding unit for addressing one of the connected memory modules by means of an address available at the address input or for making the available address available at the address output. The hub component is characterized by an error detection unit for detecting an error in a memory area of the one or more memory modules using verification data provided for said purpose.

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IPC 8 full level

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G06F 12/00 (2013.01 - KR); **G06F 12/02** (2013.01 - KR); **G11C 8/00** (2013.01 - EP KR US); **G11C 29/52** (2013.01 - KR)

Citation (search report)

See references of WO 2005017903A1

Citation (examination)

US 2002038405 A1 20020328 - LEDDIGE MICHAEL W [US], et al

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DOCDB simple family (publication)

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