

Title (en)

METHOD AND APPARATUS FOR SCRAMBLING CELL CONTENT IN AN INTEGRATED CIRCUIT

Title (de)

VERFAHREN UND VORRICHTUNG ZUM VERWÜRFELN VON ZELLENINHALT IN EINER INTEGRIERTEN SCHALTUNG

Title (fr)

PROCEDE ET APPAREIL D'EMBROUILLAGE DE CONTENU DE CELLULE DANS UN CIRCUIT INTEGRE

Publication

**EP 1652217 A4 20090513 (EN)**

Application

**EP 04777926 A 20040708**

Priority

- US 2004022146 W 20040708
- FR 0308405 A 20030709
- US 86168304 A 20040604

Abstract (en)

[origin: WO2005008729A2] The invention provides a system for scrambling data in a sequential cell. The sequential cell is configured to receive the data from a data bus. A scrambling unit is coupled to the sequential cell and the data bus. The scrambling unit is configured to receive a scrambling unit input from the data bus and produce a scrambling unit output that differs from the scrambling unit input. The scrambling unit output is transmitted to the sequential cell. A descrambling unit is coupled to the sequential cell and is configured to receive a descrambling unit input from the sequential cell and produce a descrambling unit output that differs from the descrambling unit input. The descrambling unit output is equal to the scrambling unit input.

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

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- [X] KUHN M: "The TrustNo 1 Cryptoprocessor Concept", THE TRUSTNO 1 CRYPTOPROCESSOR CONCEPT, 30 April 1997 (1997-04-30), XP002265827
- [A] MENEZES, VANSTONE, OORSCHOT: "Handbook of Applied Cryptography", 1997, CRC PRESS LLC, USA, XP002351019
- [A] LIE D ET AL: "ARCHITECTURAL SUPPORT FOR COPY AND TAMPER RESISTANT SOFTWARE", ASPLOS. PROCEEDINGS. INTERNATIONAL CONFERENCE ON ARCHITECTURAL SUPPORT FOR PROGRAMMING LANGUAGES AND OPERATING SYSTEMS, NEW YORK, NY, US, vol. 34, no. 5, 12 November 2000 (2000-11-12), pages 168 - 177, XP001018164
- See references of WO 2005008729A2

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DE FR GB IT

DOCDB simple family (publication)

**WO 2005008729 A2 20050127**; **WO 2005008729 A3 20070315**; EP 1652217 A2 20060503; EP 1652217 A4 20090513

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