

Title (en)

TRANSISTOR ARRANGEMENT, INTEGRATED CIRCUIT AND METHOD FOR OPERATING FIELD EFFECT TRANSISTORS

Title (de)

TRANSISTOR-ANORDNUNG, INTEGRIERTER SCHALTKREIS UND VERFAHREN ZUM BETREIBEN VON FELDEFFEKTTRANSISTOREN

Title (fr)

ENSEMBLE DE TRANSISTORS, CIRCUIT DE COMMUTATION INTEGRE ET PROCEDE DE FONCTIONNEMENT DE TRANSISTORS A EFFET DE CHAMP

Publication

EP 1661242 A1 20060531 (DE)

Application

EP 04786703 A 20040901

Priority

- DE 2004001941 W 20040901
- DE 10340846 A 20030904

Abstract (en)

[origin: WO2005025055A1] The transistor arrangement contains a first and a second field effect transistor comprising a first and a second source drain connection and a control connection for applying a first or a second signal. The two field effect transistors are of the same conductive type. The transistor arrangement is configured in such a manner that the first signal can be applied in an alternating manner to the control connection of the first field effect transistor and the second signal can be applied in a simultaneous manner to the control connection of the second field effect transistor, and/or the second signal can be applied to the control connection of the first field effect transistor and the first signal can be applied simultaneously to the control connection of the second field effect transistor.

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H03F 3/72; H03F 1/26

IPC 8 full level

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CPC (source: EP US)

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Citation (search report)

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DE

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