

Title (en)

INPUT/OUTPUT DEVICE FOR A CLOCK SIGNAL, IN PARTICULAR FOR THE CORRECTION OF CLOCK SIGNALS

Title (de)

TAKTSIGNAL-EIN-/AUSGABEVORRICHTUNG, INSBESONDERE ZUR KORREKTUR VON TAKTSIGNALEN

Title (fr)

DISPOSITIF D'ENTREE / SORTIE DE SIGNAL D'HORLOGE, EN PARTICULIER POUR LA CORRECTION DE SIGNAUX D'HORLOGE

Publication

**EP 1687897 A1 20060809 (DE)**

Application

**EP 04818827 A 20041112**

Priority

- EP 2004052937 W 20041112
- DE 10354818 A 20031124

Abstract (en)

[origin: DE10354818B3] The clock signal input/output device (1) has the received clock signal fed via a frequency divider (4) to a signal integrator (6), which is followed by a pair of identical signal receiver circuits (8,9), connected to a signal restoration circuit (11) delivering the clock output signal, the opposing signal flanks of which are triggered in response to signal flanks of the respective signals provided by the signal receiver circuits. An independent claim for a clock signal correction method is also included.

IPC 8 full level

**H03K 5/135** (2006.01); **G11C 7/22** (2006.01); **G11C 11/4076** (2006.01); **H03K 5/151** (2006.01); **H03K 5/156** (2006.01); **H04L 7/033** (2006.01)

CPC (source: EP US)

**G11C 7/22** (2013.01 - EP US); **G11C 7/222** (2013.01 - EP US); **G11C 11/4076** (2013.01 - EP US); **H03K 5/151** (2013.01 - EP US); **H03K 5/1565** (2013.01 - EP US)

Citation (search report)

See references of WO 2005050845A1

Designated contracting state (EPC)

DE FR GB IE IT

DOCDB simple family (publication)

**DE 10354818 B3 20050217**; CN 100379151 C 20080402; CN 1842963 A 20061004; EP 1687897 A1 20060809; JP 2007504730 A 20070301; US 2006214716 A1 20060928; US 7227396 B2 20070605; WO 2005050845 A1 20050602

DOCDB simple family (application)

**DE 10354818 A 20031124**; CN 200480024423 A 20041112; EP 04818827 A 20041112; EP 2004052937 W 20041112; JP 2006525154 A 20041112; US 37499006 A 20060315