Title (en)
METHOD FOR THE CONVERSION OF LOGICAL INTO REAL BLOCK ADDRESSES IN FLASH MEMORIES

Title (de)
VERFAHREN ZUR UMSETZUNG VON LOGISCHEN IN REALE BLOCKADRESSEN IN FLASHSPEICHERN

Title (fr)
PROCEDE POUR TRANSFORMER DES ADRESSES DE BLOCS LOGIQUES EN ADRESSES DE BLOCS REELS DANS DES MEMOIRES FLASH

Publication
**EP 1700220 A1 20060913 (DE)**

Application
**EP 05850471 A 20051220**

Priority
• EP 2005056985 W 20051220
• DE 102005001038 A 20050107

Abstract (en)
[origin: DE102005001038B3] The method involves assigning a logical block number (LBN) on a table (LTP) of a physical memory block number (PBN), and assigning the block number (PBN) of a real memory block number on another table (PTR). A set of real memory blocks is addressed with the block number (PBN). Memory operations are carried out on all real memory blocks of the block number (PBN) and the addressed blocks are located at different memory chips. An independent claim is also included for a memory system with a memory block.

IPC 8 full level
**G06F 12/02** (2006.01)

CPC (source: EP KR US)
**G06F 12/00** (2013.01 - KR); **G06F 12/02** (2013.01 - KR); **G06F 12/0246** (2013.01 - EP US); **G06F 12/06** (2013.01 - EP US)

Citation (search report)
See references of WO 2006072549A1

Citation (examination)
WO 2004040454 A2 20040513 - SANDISK CORP [US], et al

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC NL PL PT RO SE SI SK TR

DOCDB simple family (publication)
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**DE 102005001038 A 20050107**; CA 2591957 A 20051220; CN 200580046138 A 20051220; EP 05850471 A 20051220; EP 2005056985 W 20051220; JP 2007553484 A 20051220; KR 20077013809 A 20070619; TW 94145751 A 20051222; US 81354805 A 20051220