

Title (en)

Error detection & correction

Title (de)

Fehlererkennung und -korrektur

Title (fr)

Détection et correction d'erreurs

Publication

EP 1703396 A2 20060920 (EN)

Application

EP 05253847 A 20050621

Priority

JP 2005076403 A 20050317

Abstract (en)

The present invention provides a control method for an error detection & correction apparatus, comprising a plurality of data processing apparatuses (40) which reside in a data communication path (15) and partake in exchanging data by the unit of error correcting code, and a plurality of information exchange paths (50) which are installed between the plurality of data processing apparatuses (40); and generating a true syndrome by exchanging a partial syndrome relating to a part of the data partaken by each of the data processing apparatuses (40) by way of the information exchange paths (50), comprising the step of exchanging renewal information relating to a part of the data partaken by each of the data processing apparatuses (40) with the other data processing apparatus (40) by way of the information exchange path (50).

IPC 8 full level

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CPC (source: EP KR US)

G06F 11/00 (2013.01 - KR); **G06F 11/1044** (2013.01 - EP US); **G06F 11/14** (2013.01 - KR)

Citation (applicant)

- JP H0423048 A 19920127 - HITACHI LTD
- JP H11232129 A 19990827 - NIPPON ELECTRIC ENG

Designated contracting state (EPC)

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Designated extension state (EPC)

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DOCDB simple family (publication)

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JP 2006260139 A 20060928; JP 4723265 B2 20110713; KR 100717572 B1 20070515; KR 20060101140 A 20060922;
US 2006236213 A1 20061019; US 7543220 B2 20090602

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