

Title (en)

METHOD AND APPARATUS FOR OVER CLOCKING IN A DIGITAL PROCESSING SYSTEM

Title (de)

VERFAHREN UND VORRICHTUNG ZUR ÜBERTAKTUNG IN EINEM DIGITALEN VERARBEITUNGSSYSTEM

Title (fr)

PROCEDE ET DISPOSITIF DE DEPASSEMENT DE FREQUENCE D'HORLOGE DANS UN SYSTEME DE TRAITEMENT NUMERIQUE

Publication

**EP 1711878 A2 20061018 (EN)**

Application

**EP 05702729 A 20050120**

Priority

- IB 2005050234 W 20050120
- EP 04100301 A 20040128
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Abstract (en)

[origin: WO2005073828A2] A method of determining a maximum optimum clock frequency at which a digital processing system can operate, the method comprising the steps of: generating a clock signal at an initial frequency; increasing said frequency in a step-wise manner and determining the operation of said system each of a selected number of frequencies, until a clock frequency is identified at which said processor does not operate correctly; and identifying a maximum clock frequency at which said system can operate correctly; characterized in that: said maximum clock frequency comprises the frequency immediately previous to the one identified as being one at which said system does not operate correctly; and in that a timing monitor is provided for determining whether or not said system can operate within system timing constraints at each frequency, thereby indicating whether or not said system operates correctly at the respective frequency.

IPC 8 full level

**G06F 1/00** (2006.01); **G06F 1/08** (2006.01)

CPC (source: EP US)

**G06F 1/08** (2013.01 - EP US)

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