

Title (en)

METHOD AND INTEGRATED SWITCHING CIRCUIT FOR INCREASING THE IMMUNITY TO INTERFERENCE

Title (de)

VERFAHREN UND INTEGRIERTER SCHALTKEIS ZUR ERHUNG DER ST&Ouml;RUFESTIGKEIT

Title (fr)

PROCEDE ET CIRCUIT INTEGRE PERMETTANT D'AMELIORER LA RESISTANCE AUX INTERFERENCES

Publication

**EP 1723523 A1 20061122 (DE)**

Application

**EP 05716727 A 20050217**

Priority

- EP 2005050707 W 20050217
- DE 102004008809 A 20040220

Abstract (en)

[origin: WO2005081107A1] The invention relates to a method for increasing the immunity to interference of an integrated switching circuit (16). According to said method, error signals are transmitted between at least one microprocessor chip or multiple microcontroller (1) and at least one additional component (2) in the form of one or more such error signals. A minimum pulse length, which is independent of the clock pulse frequency of the microprocessor or microprocessors, is defined for said transmission. A signal on an error line with a specific pulse length that is in excess of said minimum length is interpreted as an error. The invention also relates to an integrated switching circuit, which is configured in particular to carry out the aforementioned method and comprises at least one microprocessor chip or multiple microcontroller (1) and at least one additional component (2), which contains in particular separate power components and one or more pulse spreading units and/or signal delay units for the sequential emission of error pulses (6, 6') via at least one error line (3, 4)

IPC 8 full level

**G06F 11/00** (2006.01); **G06F 11/07** (2006.01); **G06F 11/267** (2006.01); **G06F 11/16** (2006.01)

CPC (source: EP KR US)

**G06F 11/00** (2013.01 - KR); **G06F 11/07** (2013.01 - KR); **G06F 11/0724** (2013.01 - EP US); **G06F 11/0736** (2013.01 - EP US); **G06F 11/0739** (2013.01 - EP US); **G06F 11/0772** (2013.01 - EP US); **G06F 11/16** (2013.01 - KR); **G06F 11/20** (2013.01 - KR); **G06F 11/2215** (2013.01 - EP US); **G06F 11/1645** (2013.01 - EP US)

Citation (search report)

See references of WO 2005081107A1

Designated contracting state (EPC)

DE FR IT

DOCDB simple family (publication)

**WO 2005081107 A1 20050901**; CN 1922581 A 20070228; EP 1723523 A1 20061122; JP 2007535234 A 20071129; KR 20060131842 A 20061220; US 2007205930 A1 20070906; US 8578258 B2 20131105

DOCDB simple family (application)

**EP 2005050707 W 20050217**; CN 200580005220 A 20050217; EP 05716727 A 20050217; JP 2006553592 A 20050217; KR 20067016462 A 20060816; US 59008705 A 20050217