Title (en)

Pixel circuit for LCD and method of driving the same

Title (de)

LCD-Pixelschaltung und Ansteuerverfahren dafür

Title (fr)

Circuit de pixels d'un LCD et son procédé

Publication

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Application

EP 06113510 A 20060504

Priority

CN 200510070906 A 20050517

Abstract (en)

A pixel circuit (30) for an active matrix display (3) and the corresponding driving method are disclosed. The pixel circuit comprises a selection transistor (Q 1), a first storage capacitor (C 1), a synchronization unit (202), a second storage capacitor (C 2) and a light emitting element (D). During a scan period, image data is written to the first capacitor (C 1) where it is stored temporarily. Upon reception of a synchronization signal (CN1), the image data is transferred to the second capacitor (C 2) and drives the light emitting element (D). Thus all tight emitting elements (D) are illuminated simultaneously. An additional transistor (Q 2) may be provided for discharging the second capacitor (C 2) and thereby erasing image data of the previous frame before starting a new light emission phase.

IPC 8 full level

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CPC (source: EP)

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