

Title (en)  
INTEGRATED CIRCUIT DEVICE

Title (de)  
ANORDNUNG MIT EINEM INTEGRIERTEN SCHALTKREIS

Title (fr)  
DISPOSITIF A CIRCUIT INTEGRE

Publication  
**EP 1728137 A1 20061206 (DE)**

Application  
**EP 05716983 A 20050310**

Priority  
• EP 2005051072 W 20050310  
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Abstract (en)  
[origin: WO2005098567A1] The invention relates to an integrated circuit (1) provided with function modules (2) which comprise a central processing unit (4) for treating data and executing a program and a cache memory (5). Until now, it was complicated and costly to ensure the manipulation security of said modules. Said invention makes it possible to take remedy actions consisting in that the function modules (2) comprise an encoding unit (6) for data encoding and decoding.

IPC 8 full level  
**G06F 1/00** (2006.01); **G06F 21/00** (2006.01); **G06F 21/71** (2013.01); **G06F 21/72** (2013.01)

CPC (source: EP US)  
**G06F 21/71** (2013.01 - EP US); **G06F 21/72** (2013.01 - EP US); **G06F 2221/2101** (2013.01 - EP US)

Citation (search report)  
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• See references of WO 2005098567A1

Designated contracting state (EPC)  
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DOCDB simple family (publication)  
**WO 2005098567 A1 20051020**; BR PI0509073 A 20070821; CN 1934517 A 20070321; DE 102004014435 A1 20051117;  
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