

Title (en)

Display drive circuit and drive method for the same

Title (de)

Anzeigeansteuerung und Ansteuerfahren dafür

Title (fr)

Circuit de commande d'affichage et procédé de commande pour ce même circuit

Publication

**EP 1729282 A2 20061206 (EN)**

Application

**EP 06114420 A 20060523**

Priority

CN 200510075546 A 20050603

Abstract (en)

A display drive circuit and a drive method for the same are proposed. The drive circuit is alternately controlled by a first control signal and a second control signal in each frame time, and alternately performs storage or release of image data to a first maintaining capacitor and a second maintaining capacitor in an LCD panel according to scan signals. Frames stored in the first maintaining capacitor and the second maintaining capacitor in the LCD panel can therefore be continuously and alternately output for display.

IPC 8 full level

**G09G 3/36** (2006.01)

CPC (source: EP)

**G09G 3/3648** (2013.01); **G09G 3/3659** (2013.01); **G09G 2300/0814** (2013.01); **G09G 2300/0852** (2013.01); **G09G 2320/0223** (2013.01); **G09G 2320/0252** (2013.01); **G09G 2320/0257** (2013.01)

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC NL PL PT RO SE SI SK TR

Designated extension state (EPC)

AL BA HR MK YU

DOCDB simple family (publication)

**EP 1729282 A2 20061206**; **EP 1729282 A3 20090715**; CN 100449599 C 20090107; CN 1873757 A 20061206

DOCDB simple family (application)

**EP 06114420 A 20060523**; CN 200510075546 A 20050603