

Title (en)

A SYSTEM AND METHOD FOR IMPROVED SPEED ARCHITECTURE

Title (de)

SYSTEM UND VERFAHREN FÜR EINE ARCHITKTUR MIT VERBESSERTER GESCHWINDIGKEIT

Title (fr)

SYSTEME ET PROCEDE DESTINES A UNE ARCHITECTURE DE VITESSE AMELIOREE

Publication

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Application

EP 05731842 A 20050415

Priority

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- AU 2004901973 A 20040415

Abstract (en)

[origin: WO2005101142A1] A hardware control system and method for use with industrial plant or machinery, the hardware control system includes an input means for receiving information from a plurality of input lines an output means for transmitting operating instructions to a plurality of output lines, a microprocessor in communication with the input means and output means, said microprocessor capable of processing the information received and communicating resulting operating instructions to the output means, at least one hardware control line in parallel to the microprocessor being in communication with the input means and output means, said at least one hardware control line being capable of communicating the information received, from at least one of said input lines, directly to the output means, and designation means for designating which input lines will provide information to be communicated through the at least one hardware control line and which input lines will provide information to be communicated through the microprocessor. The system may be used in a safety system for industrial machinery.

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

- [X] US 4117317 A 19780926 - DOOLEY JR PHILIP G, et al
- See references of WO 2005101142A1

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