

Title (en)
OUTPUT STAGE SYSTEM

Title (de)
AUSGANGSSTUFENANORDNUNG

Title (fr)
SYSTEME D'ETAGES DE SORTIE

Publication
EP 1741185 A1 20070110 (DE)

Application
EP 05732703 A 20050421

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Abstract (en)
[origin: WO2005104370A1] Disclosed is an output stage system in which several transistors (P1, P2) are disposed in a series integrated circuit while being triggered by one respective power-controlled voltage source (Q1, Q2). This allows the transistors to be operated at different, absolute potentials while making it possible to supply a voltage at the output (A) which is greater than the rated voltage of the transistors (P1, P2). The disclosed output stage system can be produced entirely according to integrated circuit technology and is suitable for supplying digital and analog signal outputs at a high respective voltage excursion.

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