

Title (en)

LOW POWER CIRCUITS FOR ACTIVE MATRIX EMISSIVE DISPLAYS AND METHODS OF OPERATING THE SAME

Title (de)

LOW-POWER-SCHALTUNGEN FÜR AKTIVMATRIX-EMISSIONS-DISPLAYS UND VERFAHREN ZU IHREM BETRIEB

Title (fr)

CIRCUITS A FAIBLE PUISSANCE POUR DES AFFICHAGES EMISSIFS A MATRICE ACTIVE ET PROCEDES DE FONCTIONNEMENT ASSOCIES

Publication

EP 1743313 A4 20070530 (EN)

Application

EP 05734209 A 20050406

Priority

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- US 56147404 P 20040412
- US 84119804 A 20040506
- US 87234404 A 20040617

Abstract (en)

[origin: WO2005101267A2] The embodiments of the present invention provide a flat panel display having a plurality of pixels, each comprising a light-emitting device configured to emit light in accordance with a current flowing through the light-emitting device, a transistor coupled to the light-emitting device and configured to provide the current through the light-emitting device, the current increasing with a ramp voltage applied to a control terminal of the transistor, and a switching device configured to switch off in response to the luminance of the light-emitting device having reached a specified level, thereby disconnecting the ramp voltage from the transistor and locking the brightness at the specified level.. The switching device is further configured to stay off thereby allowing the luminance of the light-emitting device to be kept at the specified level until the pixel is rewritten in a different frame.

IPC 8 full level

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Citation (search report)

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- [A] US 6404137 B1 20020611 - SHODO KENZO [JP]
- See references of WO 2005101267A2

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