

Title (en)
Methods and devices for the suppression of harmonics

Title (de)
Sender/Empfänger

Title (fr)
Émetteur-récepteur

Publication
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Application
EP 06021817 A 19990129

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• EP 99902460 A 19990129
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Abstract (en)
[origin: WO9939450A1] A transceiver according to the invention has application to a baggage handling system, amongst others, and includes an electrical circuit having a modulated series resistor, in the form of resistor R(modulator). Also included is a parallel switch SW1, which is placed between the antenna and a storage capacitor in the AC part of the circuit. In other embodiments the modulated series resistor, again in the form of resistor R(modulator) and parallel switch SW1, is placed between the antenna and the storage capacitor in the DC part of the circuit. Both circuits produce the same transmitted signal, although in practice the latter is simpler to implement due to its DC operating bias. In still further embodiments a modulated series resistor is placed between the antenna and the antenna's tuning capacitor.

IPC 8 full level
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CPC (source: EP US)
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Citation (search report)
• [A] EP 0242906 A1 19871028 - NEDAP NV [NL]
• [E] EP 0910034 A2 19990421 - TEMIC SEMICONDUCTOR GMBH [DE]

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GB2507363A

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AL LT LV MK RO SI

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WO 9939450 A1 19990805; AU 2259699 A 19990816; DE 69935908 D1 20070606; DE 69935908 T2 20080110; EP 1057277 A1 20001206; EP 1057277 A4 20040623; EP 1057277 B1 20070425; EP 1753149 A2 20070214; EP 1753149 A3 20080305; EP 1753149 B1 20130313; JP 2002502178 A 20020122; JP 2007053786 A 20070301; JP 2007095062 A 20070412; JP 2011123910 A 20110623; JP 4676944 B2 20110427; JP 4981179 B2 20120718; US 2006286938 A1 20061221; US 7546092 B1 20090609; US 7711332 B2 20100504

DOCDB simple family (application)
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