Title (en)
OLED PIXEL LAYOUT

Title (de)
PIXELANORDNUNG FÜR OLED

Title (fr)
LAYOUT DE PIXEL OLED

Publication
EP 1756797 A1 20070228 (FR)

Application
EP 05857313 A 20050617

Priority
- FR 2005050456 W 20050617
- FR 0451293 A 20040618

Abstract (en)
[origin: FR2871939A1] The display has pixel matrices, which are selected by a signal provided by a selection line divided by several pixels. Each pixel comprises of a capacitor allowing to retain a signal for adjusting the light intensity produced by the pixel. A capacitor (Cs) of a predetermined pixel (P) includes an electrode connected to a selection line (SL`) of a pixel (P`) different from that of the line of the pixel (P).

IPC 8 full level
G09G 3/32 (2006.01); **H01L 27/32** (2006.01)

CPC (source: EP US)
G09G 3/3233 (2013.01 - EP US); G09G 2300/0465 (2013.01 - EP US); G09G 2300/0842 (2013.01 - EP US); G09G 2300/0876 (2013.01 - EP US); H10K 59/121 (2023.02 - EP US)

Citation (search report)
See references of WO 2006092473A1

Designated contracting state (EPC)
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Designated extension state (EPC)
AL BA HR LV MK YU

DOCDB simple family (publication)
FR 2871939 A1 20051223; **FR 2871939 B1 20070126**; EP 1756797 A1 20070228; JP 2008502933 A 20080131; US 2008054784 A1 20080306; WO 2006092473 A1 20060908

DOCDB simple family (application)
FR 0451293 A 20040618; EP 05857313 A 20050617; FR 2005050456 W 20050617; JP 2007516017 A 20050617; US 62876805 A 20050617