

Title (en)
CACHE MEMORY MANAGEMENT SYSTEM AND METHOD

Title (de)
CACHE-SPEICHER-VERWALTUNGSSYSTEM UND -VERFAHREN

Title (fr)
SYSTEME ET PROCEDE DE GESTION D'UNE MEMOIRE CACHE

Publication
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Application
EP 04757053 A 20040714

Priority
US 2004022878 W 20040714

Abstract (en)
[origin: WO2006019374A1] A cache memory method and corresponding system for twodimensional data processing, and in particular, two-dimensional image processing with simultaneous coordinate transformation is disclosed. The method uses a wide and fast primary cache memory (PCM) and a deep secondary cache memory (SCM), each with multiple banks to access data simultaneously. A dedicated prefetching logic is used to obtain pixel data from an external memory upon receiving control parameters from an external processor system (PU1), and to store that data in the PCM based on a secondary control queue. The data are then prepared in specific block sizes and in specific format, and then stored in the PCM based on optimally sized pre-fetching primary control queue. The prepared data are then read by another external processor system (PU2) for processing. The cache control logic ensures the coherency of data and control parameters at the input of the PU2.

IPC 8 full level
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CPC (source: EP KR)
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