Title (en)

CIRCUIT ARRANGEMENT FOR SUPPRESSING INTERFERING SIGNALS IN TRANSMITTED SIGNALS EMITTED BY A MODEM OF A HOUSEHOLD APPLIANCE

Title (de)

SCHALTUNGSANORDNUNG ZUR UNTERDRÜCKUNG VON STÖRSIGNALEN IN VON EINEM MODEM EINES HAUSGERÄTES ABGEGEBENEN SENDESIGNALEN

Title (fr)

CIRCUIT DE SUPPRESSION DES SIGNAUX PARASITES DANS DES SIGNAUX D'EMISSION EMIS PAR LE MODEM D'UN APPAREIL ELECTROMENAGER

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Application

EP 05767987 A 20050712

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Abstract (en)

[origin: WO2006005750A1] The invention relates to a circuit arrangement for suppressing interfering signals in transmitted signals emitted by a modem (MO) of a household appliance (HG) equipped with a transmitting device for transmitting and a receiving device for receiving data signals, said modem containing a transmitting branch and a receiving branch. To this end, a low-pass filter (TP) for attenuating the interfering signals, which have interfering signal frequencies higher than the transmitted signal frequencies and which are emitted by a transmitter unit (SB) of the modem (MO), is contained in the transmitting branch of the modem (MO). In addition, a band elimination filter (BS) that filters out only the aforementioned interfering signals is connected upstream from the low-pass filter (TP) on the side of the transmitter unit (SB).

IPC 8 full level

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CPC (source: EP KR US)

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Citation (search report)

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