Title (en)
METHOD FOR GENERATING RANDOM BINARY SEQUENCES AND DEVICE THEREFOR

Title (de)
VERFAHREN ZUR ERZEUGUNG ZUFÄLLIGER BINÄRSEQUENZEN UND EINRICHTUNG DAFÜR

Title (fr)
PROCEDE POUR GENERER DES SUITES BINAIRES ALEATOIRES ET LE DISPOSITIF DE MISE EN OEUVRE

Publication
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Application
**EP 05766637 A 20050526**

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Abstract (en)
[origin: WO2005124536A1] The invention concerns a method for generating random binary sequences from a relative jitter between two synchronous logical clock signals CLK and CLJ linked by a relation of formula (I) wherein the largest common divider PGCD(KM,KD) = 1. Said method consists in sampling the frequency logical signal fCLJ of the low-to-high transition or the high-to-low transition of the frequency signal fCLJ; storing the sampled value for a period of the CLK signal to obtain the Q signal; the Q signal is periodic of period: $TQ = KD . TCLK = KM . TCLJ$. According to said method, the jitter parameters are calculated using N binary vectors acquired from the Q signal during N periods TQ.

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**G06F 7/588** (2013.01); **H04L 9/0861** (2013.01)

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See references of WO 2005124536A1

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US 2003037079 A1 20030220 - WILBER SCOTT A [US]

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