

Title (en)

METHOD AND CIRCUIT FOR BUFFERING A STREAM OF DATA

Title (de)

VERFAHREN UND SCHALTUNG ZUM PUFFERN EINES DATENSTROMS

Title (fr)

PROCEDE ET CIRCUIT DESTINES A LA MISE EN TAMPON D'UN TRAIN DE DONNEES

Publication

EP 1784708 A1 20070516 (EN)

Application

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Priority

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Abstract (en)

[origin: WO2006003539A1] Energy still is a precious commodity for portable consumer devices. In such devices, data is usually read from a mass storage device like a harddisk, buffered in a solid state memory and then rendered (processed) for reproduction over usually headphones (and a screen when applicable). The invention is aimed at more efficient use of the buffer memory, which is usually divided in compartments. The invention is based on the recognition that individual compartments of a (e.g.) dynamic buffer memory should be emptied as fast as possible as compared to gradually reading data from various compartments of the memory. In this way, considerable amounts of energy can be saved as an inactive memory compartment consumes less power than an active memory compartment.

IPC 8 full level

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CPC (source: EP KR)

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