

Title (en)

MICRO PROCESSOR DEVICE AND METHOD FOR SHUFFLE OPERATIONS

Title (de)

MIKROPROZESSOREINRICHTUNG UND VERFAHREN FÜR SHUFFLE-OPERATIONEN

Title (fr)

DISPOSITIF MICROPROCESSEUR ET PROCEDE POUR FONCTIONNEMENTS ALLER RETOUR

Publication

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Application

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Abstract (en)

[origin: WO2006033056A2] The present invention relates to a micro processor device comprising a vector processor architecture with a functional vector processor unit comprising first memory means for storing plural index vectors and processing means, the functional vector processor unit being arranged to receive a processing instruction and at least one input vector to be processed, said first memory means being arranged to provide the processing means with one of said plural index vectors in accordance with the processing instruction, and the processing means being arranged to generate in response to said instruction at least one output vector having the elements of the at least one input vector rearranged in accordance with the one index vector provided. The functional vector processor unit further comprises pre-processing means arranged to receive a parameter and to process the elements of the one index vector dependent on said parameter before generating said at least one output vector in accordance with the processed index vector. The invention further relates to a method for processing vectors with such a functional vector-processing unit.

IPC 8 full level

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