

Title (en)

METHOD FOR PRODUCING LAYERS LOCATED ON A HYBRID CIRCUIT

Title (de)

VERFAHREN ZUM PRODUZIEREN VON AUF EINER HYBRIDSCHALTUNG BEFINDLICHEN SCHICHTEN

Title (fr)

PROCEDE D'OBTENTION DE COUCHES LOCALISEES SUR UN CIRCUIT HYBRIDE

Publication

EP 1800338 A1 20070627 (FR)

Application

EP 04791517 A 20041013

Priority

FR 2004002603 W 20041013

Abstract (en)

[origin: WO2006040419A1] The invention relates to a method for producing layers located on a hybrid circuit, wherein said hybrid circuit comprises a substrate (20) and at least one elementary circuit (22) comprising a first face and a second face by which it is hybridized to the substrate face. The inventive method consists in covering said first substrate face and each elementary circuit with a first layer (24), in removing the first layer from the first face of the elementary circuit, in covering the first face and the remaining portion of the first layer by a second layer (28) and in removing said remaining portion and the second layer which covers it. Said invention can be used for producing an antireflective or metal layer on a chip.

IPC 8 full level

H01L 21/56 (2006.01); **H01L 23/31** (2006.01); **H01L 25/16** (2006.01); **H01L 31/0216** (2006.01)

CPC (source: EP US)

H01L 25/0655 (2013.01 - EP US); **H01L 25/50** (2013.01 - EP US); **H01L 2924/0002** (2013.01 - EP US)

Citation (search report)

See references of WO 2006040419A1

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LU MC NL PL PT RO SE SI SK TR

DOCDB simple family (publication)

WO 2006040419 A1 20060420; EP 1800338 A1 20070627; JP 2008516458 A 20080515; JP 4638501 B2 20110223; US 2008045037 A1 20080221; US 7759261 B2 20100720

DOCDB simple family (application)

FR 2004002603 W 20041013; EP 04791517 A 20041013; JP 2007536207 A 20041013; US 57605604 A 20041013