

Title (en)
SIGNAL STRENGTH INDICATOR

Title (de)
SIGNALSTÄRKENINDIKATOR

Title (fr)
INDICATEUR DE PUISSANCE DE SIGNAL

Publication
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Abstract (en)
[origin: WO2006043206A1] A signal processing circuit has an analog to digital converter (31) for providing a digital signal to a processor (15) from an analog input signal that is susceptible to variations in signal power, e.g. from a radio front end (12). The device has a variable gain amplifier (13) controlled by a gain control signal based on detected signal strength. The analog to digital converter has a loop comprising a loop filter for processing the input signal. A signal strength detection circuit (32) is provided for generating the gain control signal, which signal strength detection circuit has loop signal detector for detecting the signal strength from the loop. Hence a received signal strength indicator RSSI is directly coupled to the analog to digital converter (31), avoiding the delay of signal strength detection in the digital processor.

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