Title (en)
VIDEO PROCESSING

Title (de)
VIDEOVERARBEITUNG

Title (fr)
TRAITEMENT VIDEO

Publication
EP 1812928 A4 20100331 (EN)

Application
EP 05851664 A 20051114

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• US 2005041329 W 20051114
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• US 26787505 A 20051104
• US 26759905 A 20051104
• US 26770005 A 20051104

Abstract (en)
[origin: WO2006055546A2] A latency tolerant system for executing video processing operations is described. So too is a stream processing in a video processor, a video processor having scalar and vector components and multidimensional datapath processing in a video processor.

IPC 8 full level
G06F 12/02 (2006.01); **G06F 15/00** (2006.01); **G06F 15/76** (2006.01); **G06T 1/00** (2006.01); **G06T 1/20** (2006.01); **G09G 5/36** (2006.01); **G09G 5/39** (2006.01)

CPC (source: EP KR)
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Citation (search report)
• [X] US 6239810 B1 20010529 - VAN HOOK TIMOTHY J [US], et al
• [A] US 5978838 A 19991102 - MOHAMED MOATAZ A [US], et al
• See references of WO 2006055546A2

Citation (examination)
ESPASA R ET AL: "Decoupled vector architectures", HIGH-PERFORMANCE COMPUTER ARCHITECTURE, 1996. PROCEEDINGS., SECOND INT ERNATIONAL SYMPOSIUM ON SAN JOSE, CA, USA 3-7 FEB. 1996, LOS ALAMITOS, CA, USA,IEEE COMPUT. SOC, US, 3 February 1996 (1996-02-03), pages 281 - 290, XP010162067, ISBN: 978-0-8186-7237-8, DOI: 10.1109/HPCA.1996.501193

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DOCDB simple family (application)
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