

Title (en)

LDPC ENCODER AND DECODER AND LDPC ENCODING AND DECODING METHODS

Title (de)

LDPC-KODIERER UND -DEKODIERER SOWIE VERFAHREN ZUM LDPC-KODIEREN UND -DEKODIEREN

Title (fr)

CODEUR ET DECODEUR LDPC ET PROCEDES DE CODAGE ET DE DECODAGE LDPC

Publication

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Application

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Abstract (en)

[origin: WO2006062351A1] Provided are an LDPC encoder and decoder, and LDPC encoding and decoding methods. The LDPC encoder includes: a code generating circuit that includes a memory storing a first parity check matrix and sums a first row which is at least one row of the first parity check matrix and a second row which is at least one of the remaining rows of the first parity check matrix to output a second parity check matrix; and an encoding circuit receiving the second parity check matrix and an information word to output an LDPC-encoded code word. Also the LDPC decoder includes: a code generating circuit including a memory which stores a first parity check matrix and summing a first row which is at least one row of the first parity check matrix and a second row which is at least one of the remaining rows of the first parity check matrix to output a second parity check matrix; and a decoding circuit receiving the second parity check matrix and a code word to output an LDPC-decoded information word.

IPC 8 full level

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