

Title (en)

METHOD AND CIRCUIT ARRANGEMENT FOR VERIFYING ELECTRIC CONTACTS BETWEEN A FIRST OUTPUT PIN OF A FIRST POWER CIRCUIT BREAKER OF A POWER CIRCUIT BREAKER DEVICE AND AN EXTERNAL NODE AND A SECOND OUTPUT PIN OF A SECOND POWER CIRCUIT BREAKER OF SAID POWER CIRCUIT BREAKER DEVICE AND NODE

Title (de)

VERFAHREN UND SCHALTUNGSANORDNUNG ZUM ÜBERPRÜFEN VON ELEKTRISCHEN KONTAKTIERUNGEN ZWISCHEN EINEM ERSTEN AUSGANGSPIN EINES ERSTEN LEISTUNGSSCHALTERS EINER LEISTUNGSSCHALTVORRICHTUNG UND EINEM EXTERNEN KNOTEN UND EINEM ZWEITEN AUSGANGSPIN EINES ZWEITEN LEISTUNGSSCHALTERS DER LEISTUNGSSCHALTVORRICHTUNG

Title (fr)

PROCEDE ET DISPOSITIF DE COMMUTATION POUR SURVEILLER LES CONTACTS ELECTRIQUES ENTRE UNE PREMIERE BROCHE DE SORTIE D'UN PREMIER COMMUTATEUR PLACE DANS UN DISPOSITIF DE COMMUTATION ET UN NOEUD EXTERNE ET UNE SECONDE BROCHE DE SORTIE D'UN SECOND COMMUTATEUR DU DISPOSITIF DE COMMUTATION ET DU NOEUD

Publication

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Application

EP 05816268 A 20051125

Priority

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- DE 102005004608 A 20050201

Abstract (en)

[origin: DE102005004608B3] Resistors (7,8) are connected to form voltage divider between test voltage (Vt) and earth (13). Comparators (11,12) test if electrical contact (1a) exists between output pin (2) and power switch (3) and contact (1b) exists between output pin (4) and power switch (5). Comparator (11) compares first voltage (U1) at output pin (2) with first reference voltage (Uref) and second comparator (12) compares second voltage (U2) at output pin (4) with reference voltage. An interruption of contact 1a or 1b indicated by the voltage logic level, shows that the device (6) is faulty. An independent claim is also included for a circuit arrangement for verifying of electrical contact between an output pin (2,4) of a power switch (3,5) and an external node.

IPC 8 full level

G01R 19/165 (2006.01); **G01R 31/74** (2020.01); **H03K 17/12** (2006.01)

CPC (source: EP KR US)

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Citation (search report)

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Citation (examination)

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