Title (en)

METHOD FOR COATING A VARISTOR BLOCK WITH AN ELECTRICALLY INSULATING COATING, AND VARISTOR BLOCK FOR A SURGE ARRESTER

Title (de)

VERFAHREN ZUR UMMANTELUNG EINES VARISTORBLOCKES MIT EINER ELEKTRISCH ISOLIERENDEN UMHÜLLUNG SOWIE VARISTORBLOCK FÜR EINEN ÜBERSPANNUNGSABLEITER

Title (fr)

PROCEDE DE GAINAGE D'UN BLOC DE VARISTANCE AU MOYEN D'UNE GAINE ELECTRIQUEMENT ISOLANTE ET BLOC DE VARISTANCE POUR LIMITEUR DE SURTENSION

Publication

EP 1846932 A1 20071024 (DE)

Application

EP 06708002 A 20060203

Priority

- EP 2006050646 W 20060203
- DE 102005007146 A 20050211

Abstract (en)

[origin: WO2006084822A1] Disclosed are a method for coating a varistor block with an electrically insulating coating as well as a varistor block for a surge arrester. A varistor block (1) is formed from several varistor elements (2a, 2b, 2c, 2d) while an electrically insulating coating (5, 5a, 5b) is disposed around the varistor elements (2a, 2b, 2c, 2d). The electrically insulating coating (5, 5a, 5b) rests directly on a surface of the varistor block (1). Unwanted gas molecules are removed from the joining area between a surface of the varistor block (1) and the coating (5, 5a, 5b) before or while the electrically insulating coating (5, 5a, 5b) is applied to the varistor block (1)

IPC 8 full level

H01C 1/02 (2006.01); **H01C 1/034** (2006.01); **H01C 7/10** (2006.01); **H01C 7/112** (2006.01)

CPC (source: EP KR US)

H01C 1/02 (2013.01 - EP US); **H01C 1/034** (2013.01 - EP US); **H01C 7/10** (2013.01 - EP KR US); **H01C 7/112** (2013.01 - EP US)

Citation (search report)

See references of WO 2006084822A1

Designated contracting state (EPC)

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WO 2006084822 A1 20060817; BR PI0608280 A2 20091215; CN 101116153 A 20080130; CN 101116153 B 20100728; DE 102005007146 A1 20060824; EP 1846932 A1 20071024; KR 20070108236 A 20071108; RU 2007133804 A 20090320; RU 2382428 C2 20100220; US 2008136578 A1 20080612

DOCDB simple family (application)

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