

Title (en)

Input sense line for low headroom regulators

Title (de)

Eingangsabfrageleitung für Low-Headroom-Regler

Title (fr)

Ligne de capteur d'entrée pour les régulateurs de dégagement

Publication

EP 1847901 A1 20071024 (EN)

Application

EP 06008131 A 20060419

Priority

EP 06008131 A 20060419

Abstract (en)

The invention relates to a voltage regulator circuit 1 for providing a regulated output voltage. The regulator circuit 1 comprises an input pad 2, an output pad 3 and a first active device 6 for regulating the output voltage. The voltage regulator circuit is characterised in that a second active device 7 is arranged between the input pad 2 and output pad 3. The first active device 6 and the second active device 7 are connected in parallel between the input pad 2 and output pad 3. The invention accounts for voltage drops on the input line.

IPC 8 full level

G05F 1/59 (2006.01)

CPC (source: EP)

G05F 1/59 (2013.01)

Citation (search report)

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- [A] US 5631548 A 19970520 - SUSAK DAVID M [US]
- [A] US 2002080675 A1 20020627 - FEURLE ROBERT [DE]
- [A] PATENT ABSTRACTS OF JAPAN vol. 008, no. 068 (P - 264) 30 March 1984 (1984-03-30)

Cited by

CN109074111A; US9886048B2; TWI416298B

Designated contracting state (EPC)

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DOCDB simple family (publication)

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